Product Specification

M185XW01 VF

AU OPTRONICS CORPORATION

() Preliminary Specification
(V) Final Specification

Module	18.5" Color TFT-LCD
Model Name	M185XW01 VF

Customer	Date	Approved by	Date
		ChiYin Wu	2011/3/11
Approved by		**Prepared by**	
		Derec Yang	2011/3/11
Note: This Specification is subject to change without notice.		Desktop Display Business Group / AU Optronics corporation	

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Record of Revision

Version and Date	Page	Old description	New Description	Remark
0.1 2011/1/17	All	First Edition for Customer	N/A	
1.0 2011/2/25	5,6, 12, 28, 29		Upate Power consumption and Weight in page 5. Upate CIM in page 6. Upate Power Specification in page 12 Update mechanical characteristic in page 28,29	
1.1 2011/3/11	11		Update Absolute Ratings of Environment	1.1 2011/3/11

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1.0 Handling Precautions

1) Since front polarizer is easily damaged, pay attention not to scratch it.

2) Be sure to turn off power supply when inserting or disconnecting from input connector.

3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.

4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.

5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.

6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.

7) Do not open or modify the Module Assembly.

8) Do not press the reflector sheet at the back of the module to any directions.

9) In case if a Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the LED lightbar edge. Instead, press at the far ends of the LED light bar edge softly. Otherwise the TFT Module may be damaged.

10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.

11) After installation of the TFT Module into an enclosure, do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.

12) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.

13) Please avoid touching COF position while you are doing mechanical design.

14) When storing modules as spares for a long time, the following precaution is necessary:
Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between $5℃$ and $35℃$ at normal humidity.

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2.0 General Description

This specification applies to the 18.5 inch-wide Color a-Si TFT-LCD Module M185XW01.The display supports the WXGA - 1366(H) x 768(V) screen format and 16.7M colors (RGB 6-bits + Hi-FRC data). All input signals are 1-channel LVDS interface and this module doesn't contain an inverter board for backlight.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25℃ condition:

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	470.1(18.51")
Active Area	[mm]	409.8 (H) x 230.4 (V)
Pixels H x V		1366(x3) x 768
Pixel Pitch	[um]	300 (per one triad) × 300
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		TN Mode, Normally White
White Luminance (Center)	[cd/m^2]	250 cd/m^2 (Typ.)
Contrast Ratio		1000 (Typ.)
Optical Response Time	[msec]	5ms (Typ., on/off)
Nominal Input Voltage VDD	[Volt]	+5.0 V (Typ)
Power Consumption (VDD line + LED line)	[Watt]	10.3 (Typ.) (without inverter, all black pattern)
Weight	[Grams]	1290 (Typ)
Physical Size	[mm]	430.4 (W) x 254.6 (H) Typ. x 9.9 (D) Typ
Interface		One channel LVDS
Support Color		16.7M colors (RGB 6-bit + Hi_FRC)
Surface Treatment		Anti-Glare, 3H
Temperature Range Operating Storage (Shipping)	[^{o}C] [^{o}C]	0 to +50 -20 to +60
TCO5.1 Compliance		TCO5.1 Compliance
RoHS Compliance		RoHS Compliance

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2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25℃:

| Item	Unit	Conditions	Min.	Typ.	Max.	Note
Viewing Angle	[degree]	Horizontal (Right) CR = 10 (Left)	75 75	85 85	- -	2
		Vertical (Up) CR = 10 (Down)	70 70	80 80	- -	
Contrast ratio		Normal Direction	600	1000	-	3
Response Time	[msec]	Raising Time (T_{rR})	-	3.6	5.7	4
		Falling Time (T_{rF})	-	1.4	2.3	
		Raising + Falling	-	5	8	
Color / Chromaticity Coordinates (CIE)		Red x	0.614	0.644	0.674	5
		Red y	0.301	0.331	0.361	
		Green x	0.306	0.336	0.366	
		Green y	0.592	0.622	0.652	
		Blue x	0.126	0.156	0.186	
		Blue y	0.019	0.049	0.079	
Color Coordinates (CIE) White		White x	0.283	0.313	0.343	
		White y	0.299	0.329	0.359	
Central Luminance	[cd/m^2]		200	250	-	6
Luminance Uniformity	[%]		75	80	-	7
Crosstalk (in 60Hz)	[%]				1.5	8
Flicker	dB				-20	9

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Note 1: **Measurement method**

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring (at surface 35□). In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room.

Photo detector

Measured distance

LCD Panel

TFT-LCD Module

Center of the screen
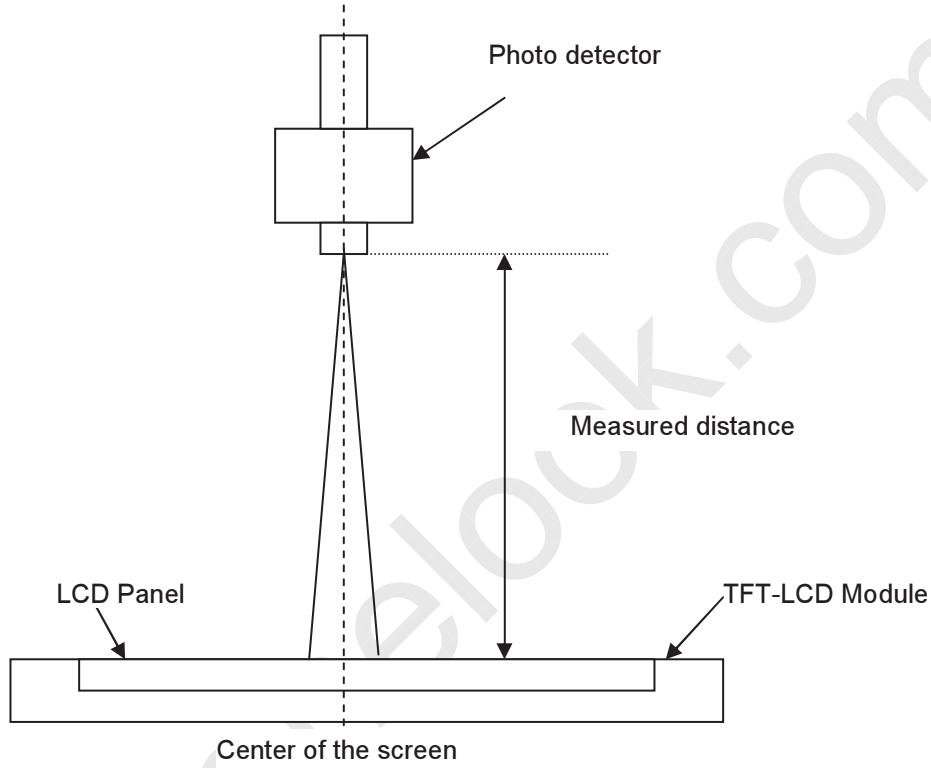
Note 2: **Definition of viewing angle** measured by ELDIM (EZContrast 88)

Viewing angle is the measurement of contrast ratio $\geqq 10$, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as follows; 90° (θ) horizontal left and right and 90° (Φ) vertical, high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated about its center to develop the desired measurement viewing angle.

Normal Line
$\phi = 0°$, $\theta = 0°$

θ_L θ_R

ϕ_H

ϕ_L

12 O'clock direction
y $\phi_H = 90°$

$\theta_L = 90°$ X

6 O'clock direction
$\phi_L = 90°$

y'

x'

$\theta_R = 90°$
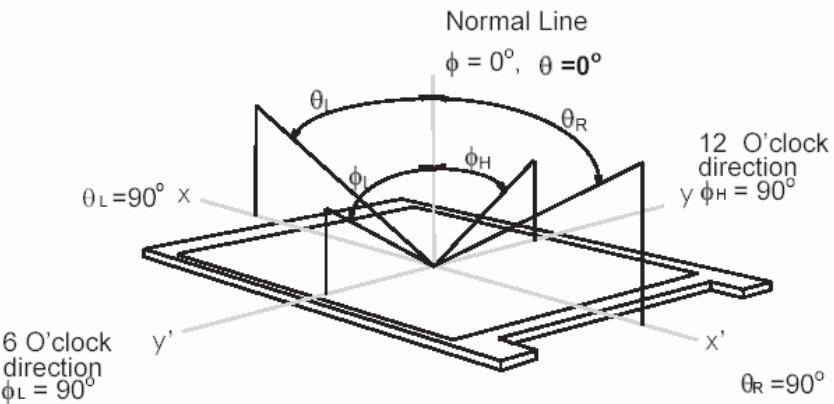
Note 3: **Contrast ratio is** measured by TOPCON SR-3

Note 4: **Definition of Response time** measured by Westar TRD-100A

The output signals of photo detector are measured when the input signals are changed from "Full Black" to "Full White" (rising time, Tr_R), and from "Full White" to "Full Black" (falling time, Tf_F), respectively. The response time is interval between the 10% and 90% (1 frame at 60 Hz) of amplitudes.
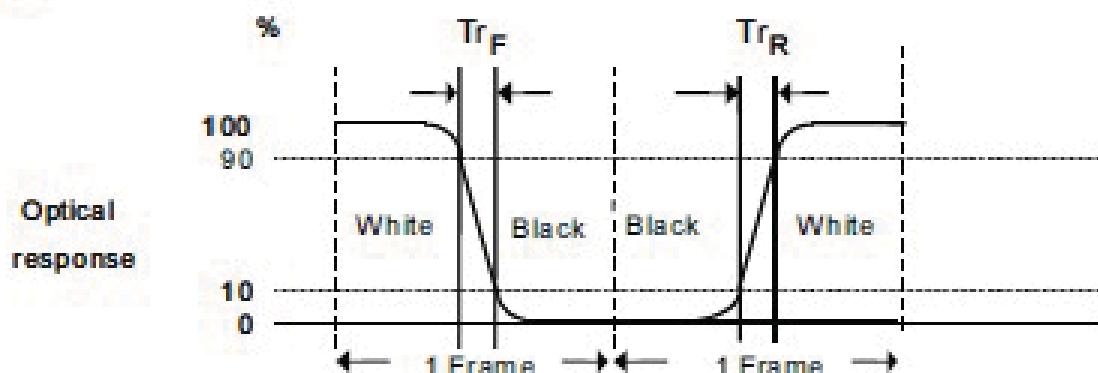
$Tr_R + Tf_F = 5$ msec (typ.).

Note 5: **Color chromaticity and coordinates (CIE) is** measured by TOPCON SR-3

Note 6: **Central luminance is** measured by TOPCON SR-3

Note 7: **Luminance uniformity of these 9 points is defined as below and** measured by TOPCON SR-3
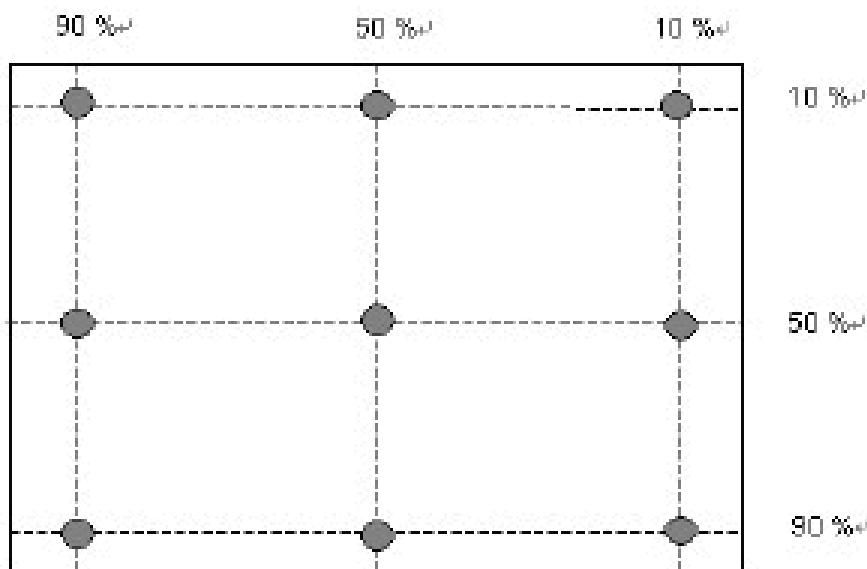
$$\text{Uniformity} = \frac{\text{Minimum Luminance in 9 points } (1-9)}{\text{Maximum Luminance in 9 Points } (1-9)}$$

Note 8: **Crosstalk is defined as below and** measured by TOPCON SR-3

$$CT = |\, YB - YA \,| / YA \times 100 \, (\%)$$

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Where

YA = Luminance of measured location without gray level 0 pattern (cd/m2)

YB = Luminance of measured location with gray level 0 pattern (cd/m2)
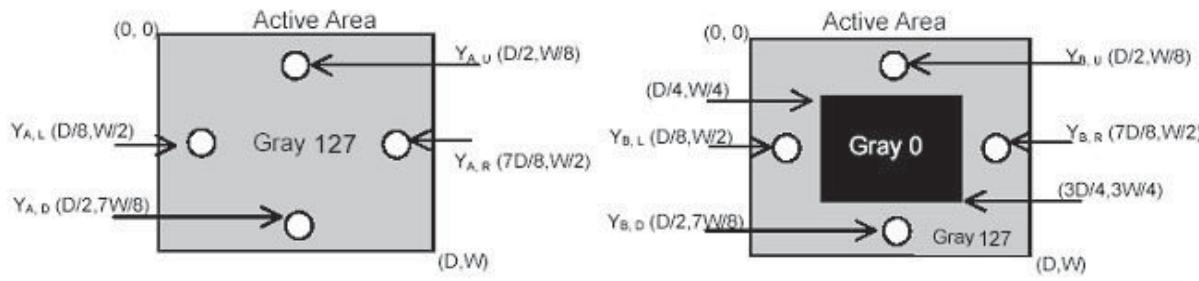
Note 9: **Test Patern: Subchecker Pattern** measured by TOPCON SR-3

Gray Level = L127

Gray Level = L0
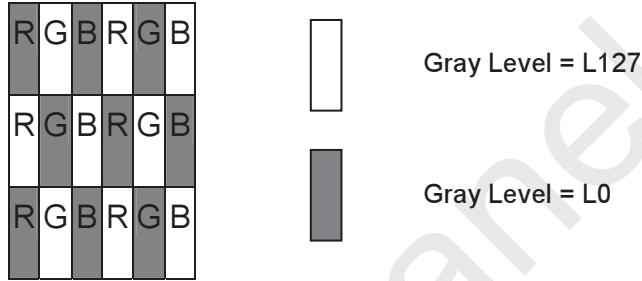
Method: Record dBV & DC value with TRD-100
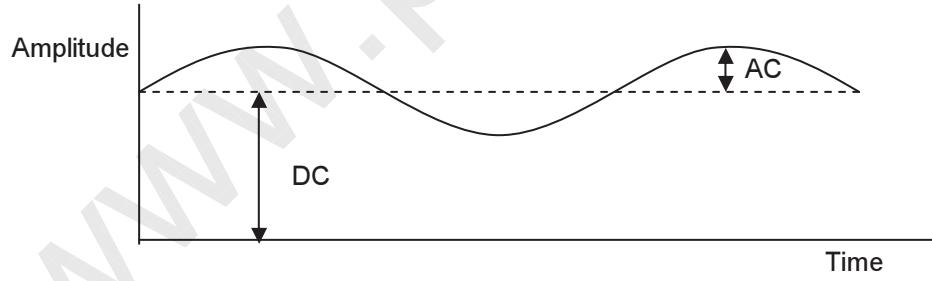
$$\text{Flicker (dB)} = 20\log\frac{AC\ \text{Level(at 30 Hz)}}{DC\ \text{Level}}$$

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3.0 Functional Block Diagram

The following diagram shows the functional block of the 18.5 inch Color TFT-LCD Module:
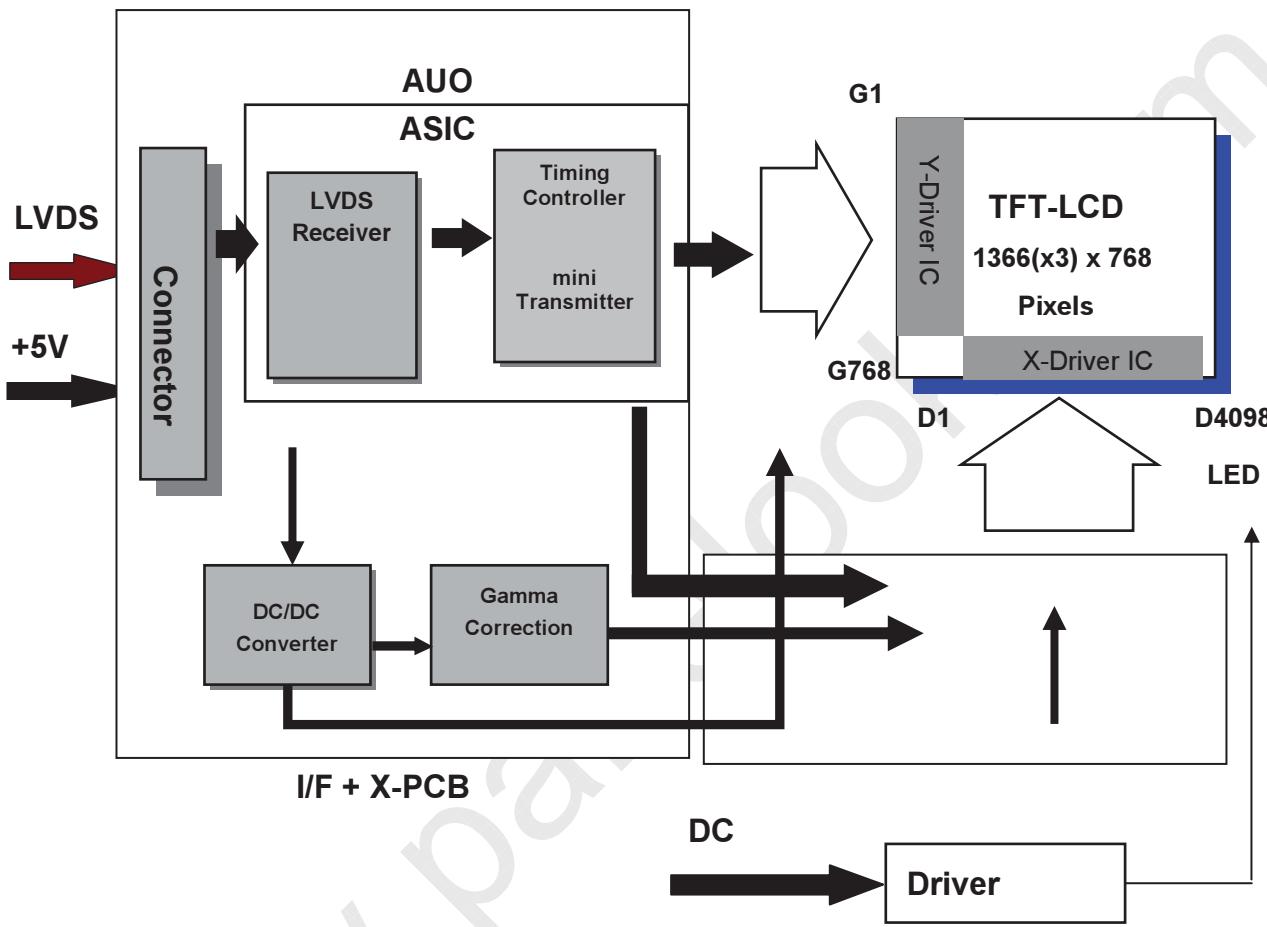
I/F PCB Interface:

093F30-00T01A-M4(STARCONN) /01-187121-30091-3(A) (P-TWO)

Mating Type:

FI-X30HL(JAE) (Locked Type)

FI-X30H (JAE)(Unlocked Type)

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4.0 Absolute Maximum Ratings

Absolute maximum ratings of the module are as following:

4.1 TFT LCD Module

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	VDD	0	+5.5	[Volt]	*Note 1,2*

4.2 Backlight Unit

Item	Symbol	Min	Max	Unit	Conditions
LED Current	ILED	-	63	[mA]	*Note 1,2*

4.3 Absolute Ratings of Environment

| Item	Symbol	Min.	Max.	Unit	Conditions
Operating Temperature	TOP	0	+50	[^{o}C]	*Note 3*
Glass surface temperature (operation)	TGS	0	+65	[^{o}C]	*Note 3, Note 4*
Operation Humidity	HOP	5	90	[%RH]	*Note 3*
Storage Temperature	TST	-20	+60	[^{o}C]	
Storage Humidity	HST	5	90	[%RH]	

Note 1: With in Ta (25℃)
Note 2: Permanent damage to the device may occur if exceeding maximum values
Note 3: Temperature and relative humidity range are shown as the below figure.

　　　　1. 90% RH Max (Ta ≦39℃)
　　　　2. Max wet-bulb temperature at 39℃ or less. (Ta ≦39℃)
　　　　3. No condensation
Note 4: *Function Judged only*
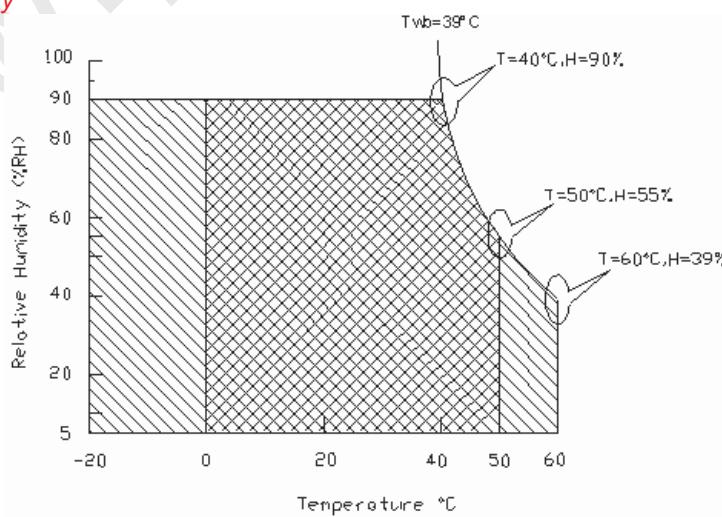
Operating Range   ▨▨▨▨   Storage Range   ▨▨▨▨ + ▧▧▧▧

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5.0 Electrical characteristics

5.1 TFT LCD Module

5.1.1 Power Specification

Input power specifications are as following:

Symbol	Parameter	Min	Typ	Max	Unit	Conditions
VDD	Logic/LCD Drive Voltage	4.5	5.0	5.5	[Volt]	+/-10%
IDD	Input Current	-	0.35	0.42	[A]	VDD= 5.0V, All Black Pattern At 60Hz,
		-	0.42	0.5		VDD= 5.0V, All Black Pattern At 75Hz
PDD	VDD Power	-	1.75	2.1	[Watt]	VDD= 5.0V, All Black Pattern At 60Hz
		-	2.1	2.5		VDD= 5.0V, All Black Pattern At 75Hz
IRush	Inrush Current	-	-	2	[A]	Note 1
VDDrp	Allowable Logic/LCD Drive Ripple Voltage	-	-	350	[mV] p-p	VDD= 5.0V, All Black Pattern At 75Hz

Note 1: Measurement conditions:

The duration of rising time of power input is 470us.
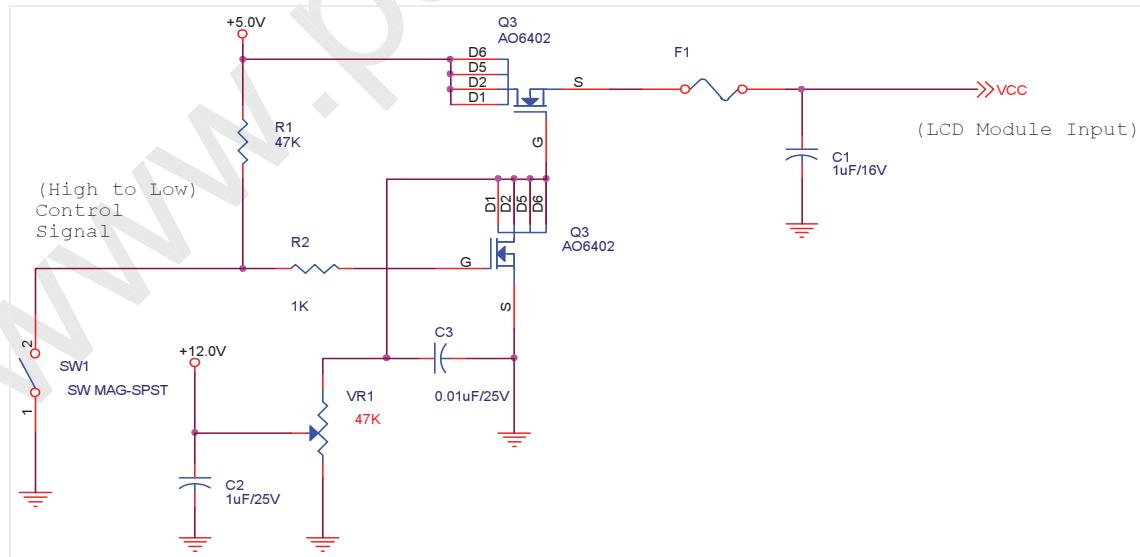
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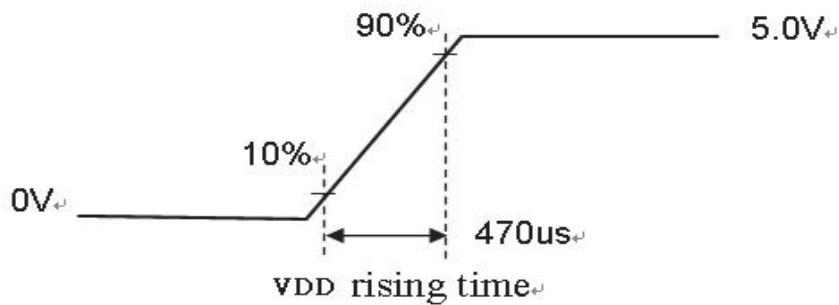
5.1.2 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off. Please refer to specifications of SN75LVDS82DGG (Texas Instruments) in detail.

1. DC Characteristics of each signal are as following:

| Symbol	Parameter	Min	Typ	Max	Units	Condition
VTH	Differential Input High Threshold	-	-	+100	[mV]	VICM = 1.2V *Note 1*
VTL	Differential Input Low Threshold	-100	-	-	[mV]	VICM = 1.2V *Note 1*
｜VID｜	Input Differential Voltage	100	-	600	[mV]	*Note 1*
VICM	Differential Input Common Mode Voltage	+1.0	+1.2	+1.5	[V]	VTH-VTL = 200MV (max) *Note 1*

Note 1: LVDS Signal Waveform

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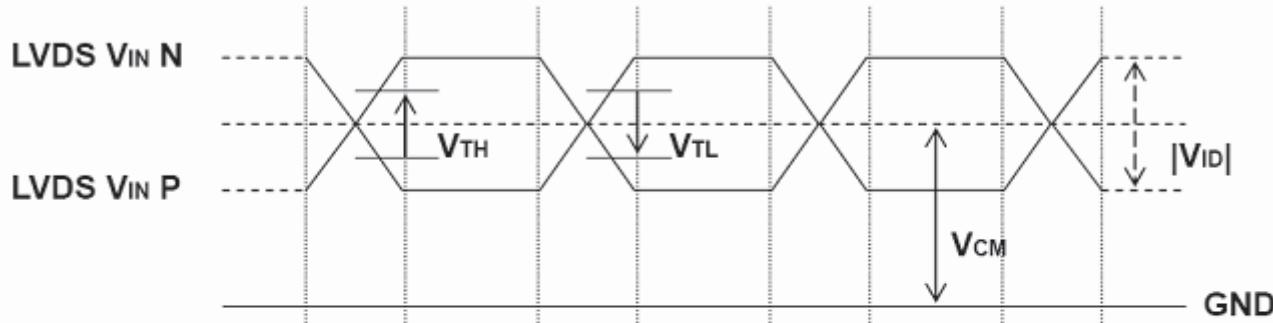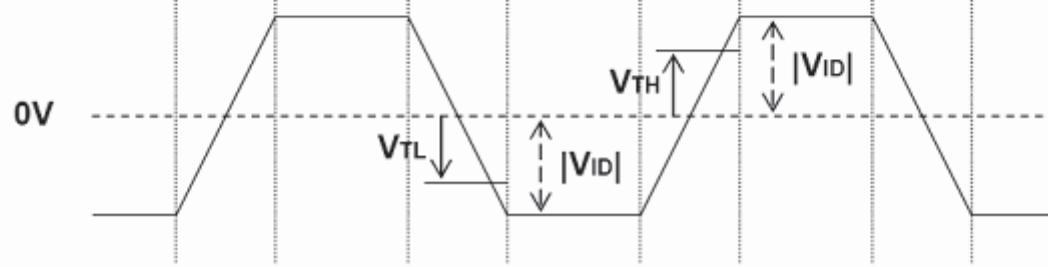
2. AC Characteristics

Description	Symbol	Min	Max	Unit	Note
Maximum deviation of input clock frequency during SSC	F_{DEV}	-	$\pm\ 3$	%	
Maximum modulation frequency of input clock during SSC	F_{MOD}	-	200	KHz	

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< Spread Spectrum>
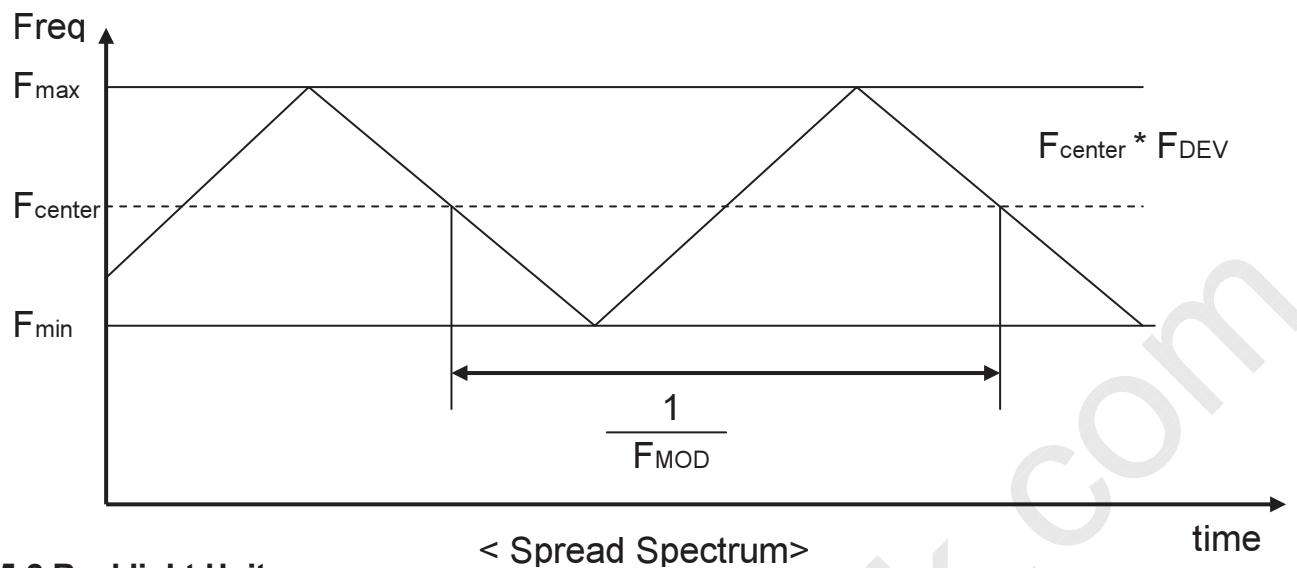
5.2 Backlight Unit

Parameter guideline for LED driving is under stable conditions at 25℃ (Room Temperature):

Symbol	Parameter	Min.	Typ.	Max.	Unit	Note
IR_{LED}	LED Operation Current	57	60	63	[mA] Note 1	Operating with fixed driving current
V_{LB}	Light Bar Operation Voltage (for reference)	30	34	36	[Volt] Note 2	
P_{BLU}	BLU Power consumption (for reference)	6.8	8.2	9.1	[Watt]	
LT_{LED}	LED life Time	30000	-	-	[Hour] Note 3	

Note 1 :The specified current is input LED chip 100% duty current.

Note 2 : The value showed in the table is one light bar's operation voltage.

Note 3 : Definition of life time : brightness becomes 50% of its original value. The minimum life time of LED unit is on the condition of IR_{LED} = 60mA and 25±2℃(Room temperature).

Note 4: Each LED light bar consists of 40 pcs LED package (4 strings x 10 pcs / string).

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6.0 Signal Characteristic

6.1 Pixel Format Image

Following figure shows the relationship of the input signals and LCD pixel format.

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6.2 The input data format

LVDS Data Mapping of NS Format
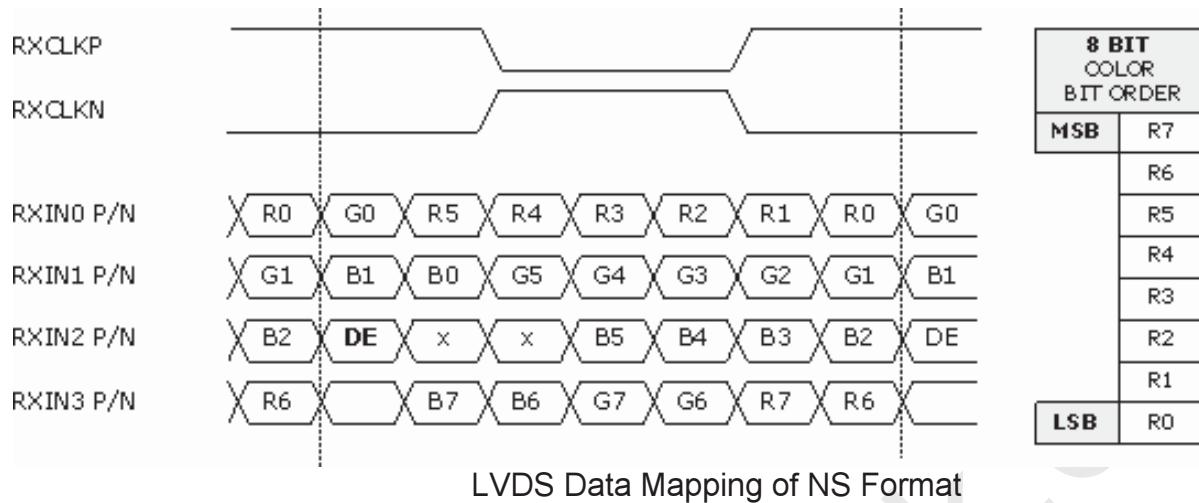
Note1: Normally, DE, VS, HS on EVEN channel are not used.
Note2: 8-bits signal input.

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6.3 Signal Description

The module using one LVDS receiver SN75LVDS82(Texas Instruments). LVDS is a differential signal technology for LCD interface and high speed data transfer device. LVDS transmitters shall be SN75LVDS83(negative edge sampling). The first LVDS port(RxOxxx) transmits odd pixels while the second LVDS port(RxExxx) transmits even pixels.

| PIN #	SIGNAL NAME	DESCRIPTION
1	NC	No contact (For AUO internal use)
2	NC	No contact (For AUO internal use)
3	NC	No contact (For AUO internal use)
4	GND	Power Ground
5	RXIN0-	Negative LVDS differential data input (0)
6	RXIN0+	Positive LVDS differential data input (0)
7	GND	Power Ground
8	RXIN1-	Negative LVDS differential data input (1)
9	RXIN1+	Positive LVDS differential data input (1)
10	GND	Power Ground
11	RXIN2-	Negative LVDS differential data input (2)
12	RXIN2+	Positive LVDS differential data input (2)
13	GND	Power Ground
14	RXCLKIN-	Negative LVDS differential clock input (clock)
15	RXCLKIN+	Positive LVDS differential data input (clock)
16	GND	Power Ground
17	RXIN3-	Negative LVDS differential data input (3)
18	RXIN3+	Positive LVDS differential data input (3)
19	GND	Power Ground
20	NC	No contact (For AUO internal use)
21	NC	No contact (For AUO internal use)
22	NC	No contact (For AUO internal use)
23	GND	Power Ground
24	GND	Power Ground
25	GND	Power Ground
26	VCC	+5V power supply
27	VCC	+5V power supply
28	VCC	+5V power supply
29	VCC	+5V power supply
30	VCC	+5V power supply

Note1: Start from left side

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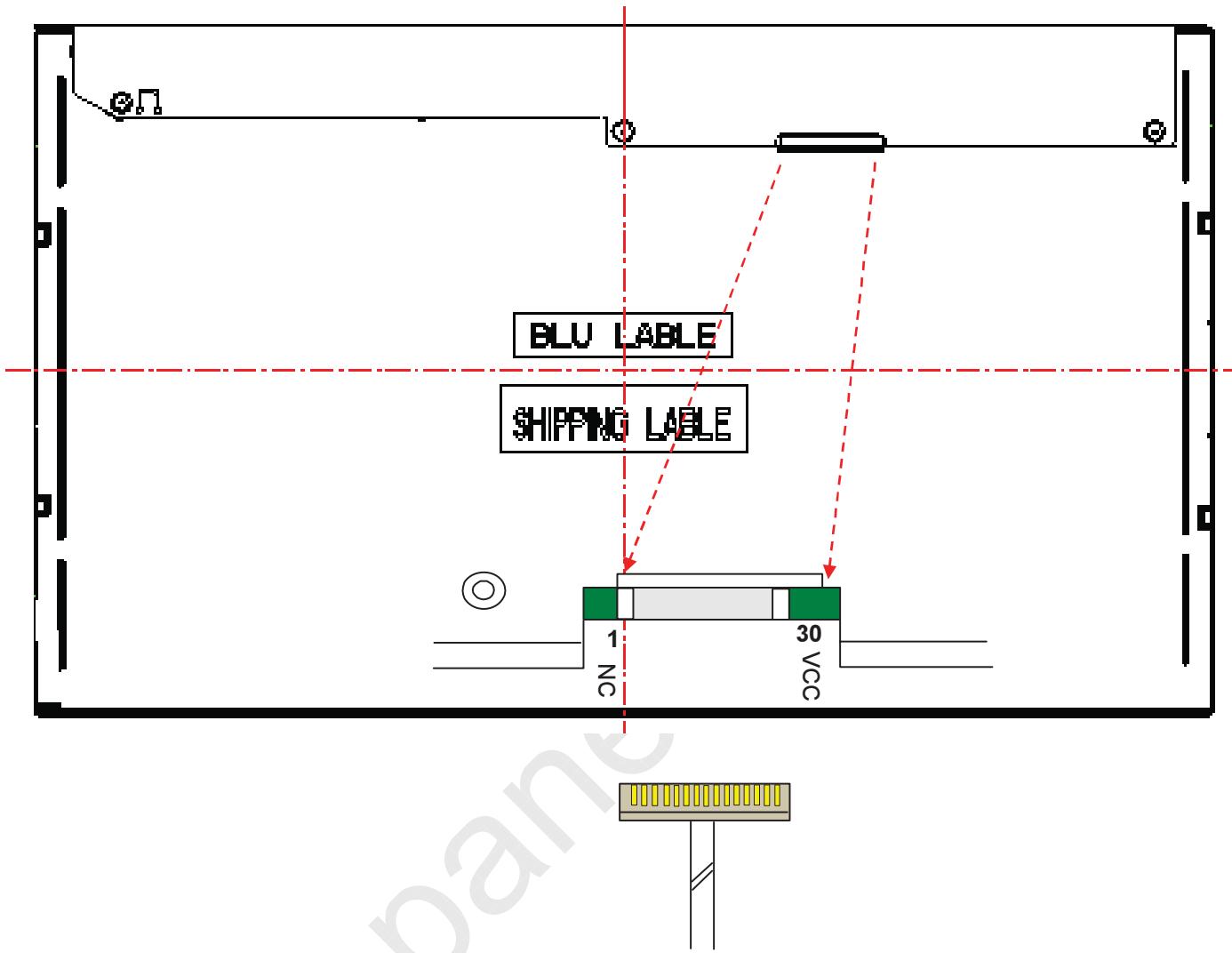
Note2: Input signals of clock shall be the same timing.

Note3: Please follow TV VESA Pin Assignment.

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6.4 Timing Characteristics

Basically, interface timing described here is not actual input timing of LCD module but close to output timing of SN75LVDS82DGG (Texas Instruments) or equivalent.

Signal	Item	Symbol	Min	Typ	Max	Unit
V-section	Period	Tv	776	808	1023	Th
	Active	Tdisp(v)	768	768	768	Th
	Blanking	Tblk(v)	8	40	255	Th
H-section	Period	Th	1416	1606	2047	Tclk
	Active	Tdisp(h)	1366	1366	1366	Tclk
	Blanking	Tblk(h)	50	240	681	Tclk
Clock	Period	Tclk	-	12.8	-	ns
	Frequency	Freq	55	78	90	MHz
Frame Rate	Frame Rate	F	50	60	75	Hz

Note1 : DE mode only
*Note2 :* Clock Frequency 90MHz(Max.)= 1416(H)*847(V)*75Hz

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6.5 Timing diagram
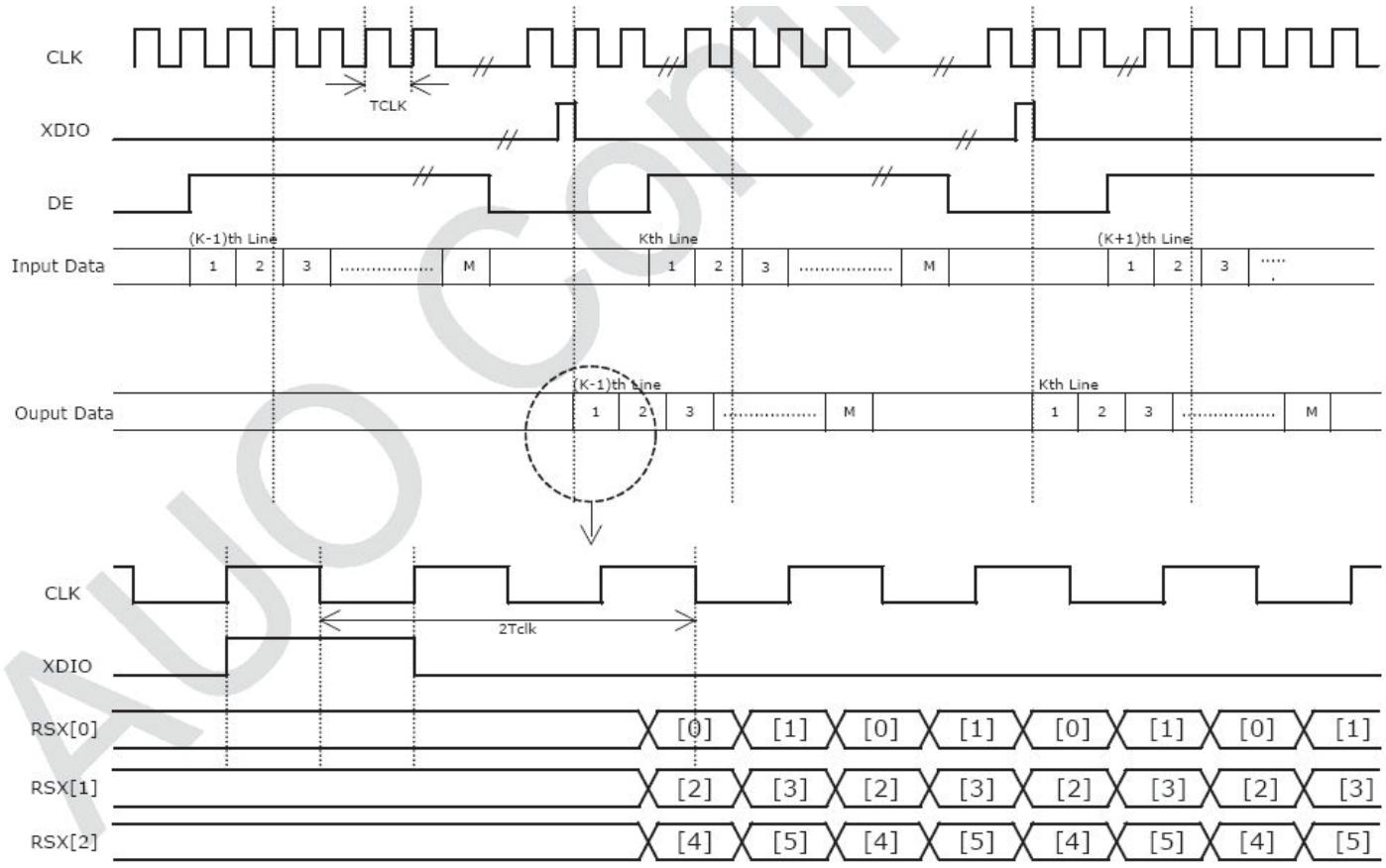
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6.6 Power ON/OFF Sequence

VDD power and lamp on/off sequence are as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.
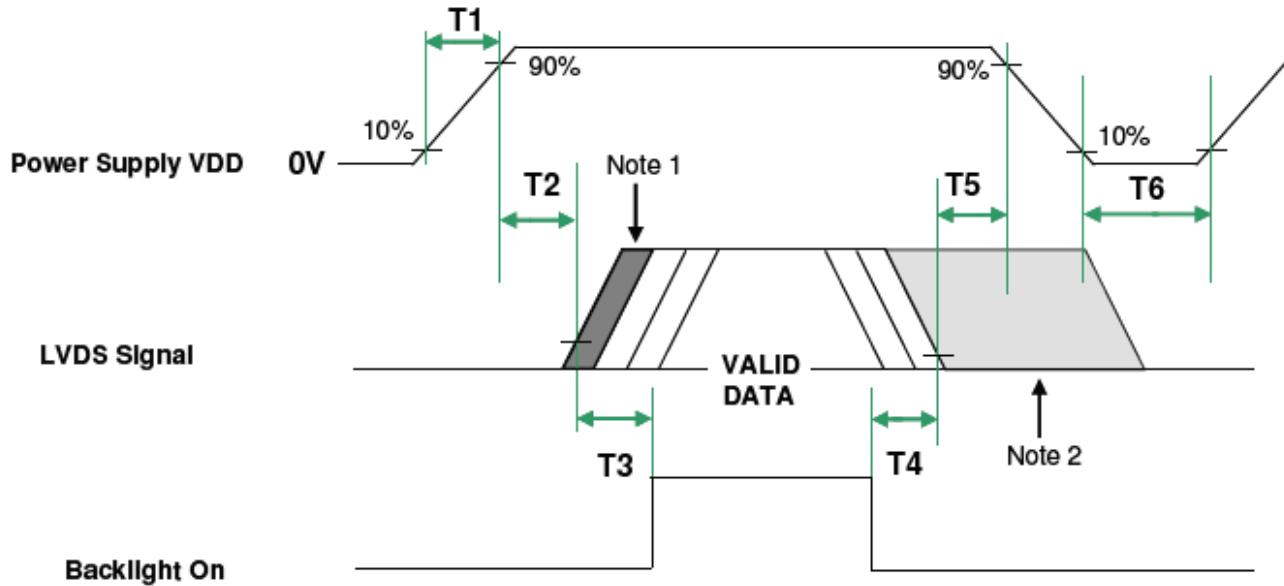
Note 1: Insert a black pattern

Note 2: Insert a white pattern after valid data and last until VDD falls to 10%

Parameter	Value		Unit
	Min.	Max.	
T1	0.5	10	[msec]
T2	0	50	[msec]
T3	500	-	[msec]
T4	200	-	[msec]
T5	40	-	[msec]
T6	1000	-	[msec]

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7.0 Connector & Pin Assignment

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Module

Connector Name / Designation	Interface Connector / Interface card
Manufacturer	STARCONN or P-TWO
Type Part Number	093F30-00T01A-M4(STARCONN) /01-187121-30091-3(A) (P-TWO)
Mating Housing Part Number	FI-X30HL(JAE) (Locked Type) FI-X30H (JAE)(Unlocked Type)

7.1.1 Pin Assignment

Pin#	Signal Name	Pin#	Signal Name
1	NC	2	NC
3	NC	4	GND
5	RXIN0-	6	RXIN0+
7	GND	8	RXIN1-
9	RXIN1+	10	GND
11	RXIN2-	12	RXIN2+
13	GND	14	RXCLKIN-
15	RXCLKIN+	16	GND
17	RXIN3-	18	RXIN3+
19	GND	20	NC
21	NC	22	NC
23	GND	24	GND
25	GND	26	VCC
27	VCC	28	VCC
29	VCC	30	VCC

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7.2 Recommend connector for Backlight Unit

This connector is mounted on the monitor system board for LED light-bar Wire mating.

Connector Name / Designation	Driver Board Connector
Manufacturer	ENTERY (E&T) Corporation
Mating Type Part Number	**3707K-Q06N-01R**

7.2.1 Pin assignment

Pin no.	Signal name
1	IRLED (current out)
2	IRLED (current out)
3	VLED (voltage in)
4	VLED (voltage in)
5	IRLED (current out)
6	IRLED (current out)

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8.0 Reliability Test

Environment test conditions are listed as following table.

Items	Required Condition	Note
Temperature Humidity Bias (THB)	Ta= 50□, 80%RH, 300hours	
High Temperature Operation (HTO)	Ta= 50□, 50%RH, 300hours	
Low Temperature Operation (LTO)	Ta= 0□, 300hours	
High Temperature Storage (HTS)	Ta= 60□, 300hours	
Low Temperature Storage (LTS)	Ta= -20□, 300hours	
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Sweep: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)	
Drop Test	Height: 60 cm, package test	
Thermal Shock Test (TST)	-20□/30min, 60□/30min, 100 cycles	*1*
On/Off Test	On/10sec, Off/10sec, 30,000 cycles	
ESD (Electro Static Discharge)	Contact Discharge: ± 8KV, 150pF(330Ω) 1sec, 8 points, 25 times/ point. Air Discharge: ± 15KV, 150pF(330Ω) 1sec 8 points, 25 times/ point.	*2*
Altitude Test	Operation:10,000 ft Non-Operation:30,000 ft	

Note 1: The TFT-LCD module will not sustain damage after being subjected to 100 cycles of rapid temperature change. A cycle of rapid temperature change consists of varying the temperature from -20℃ to 60℃, and back again. Power is not applied during the test. After temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 2: EN61000-4-2, ESD class B: Certain performance degradation allowed
　　　　　　　　　　No data lost
　　　　　　　　　　Self-recoverable
　　　　　　　　　　No hardware failures.

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9.0 Shipping Label

The label is on the panel as shown below:
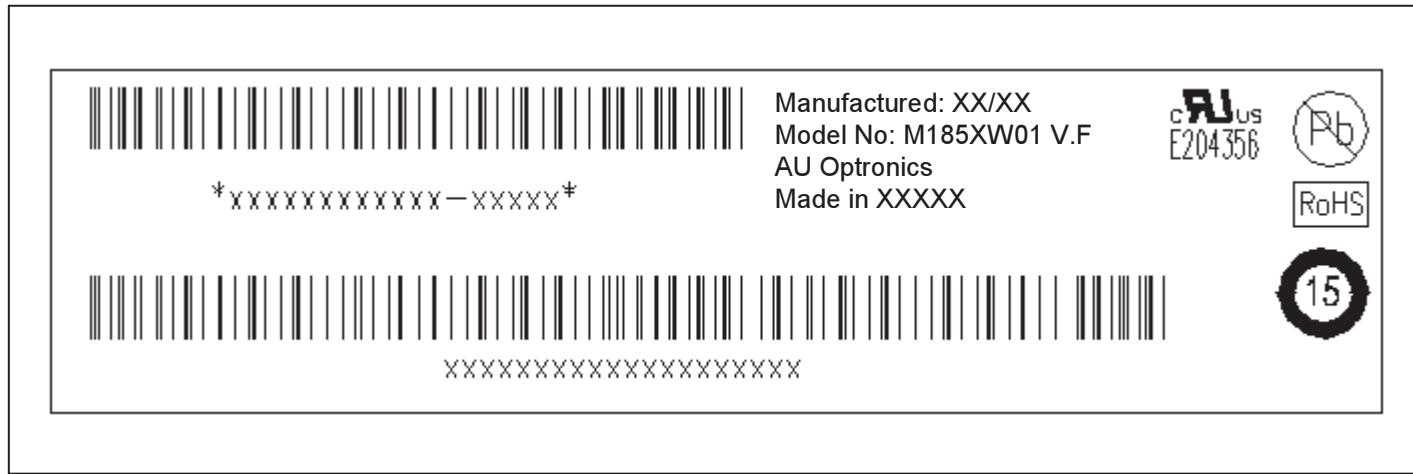
Note 1: For Pb Free products, AUO will add [Pb symbol] for identification.

Note 2: For RoHS compatible products, AUO will add [RoHS symbol] for identification.

Note 3: For China RoHS compatible products, AUO will add [15 symbol] for identification.

**Note 4:** The Green Mark will be presented only when the green documents have been ready by AUO Internal Green Team.

Product Specification

M185XW01 VF

AU OPTRONICS CORPORATION

10.0 Mechanical Characteristics

Avoid touching COF position when doing mechanical design
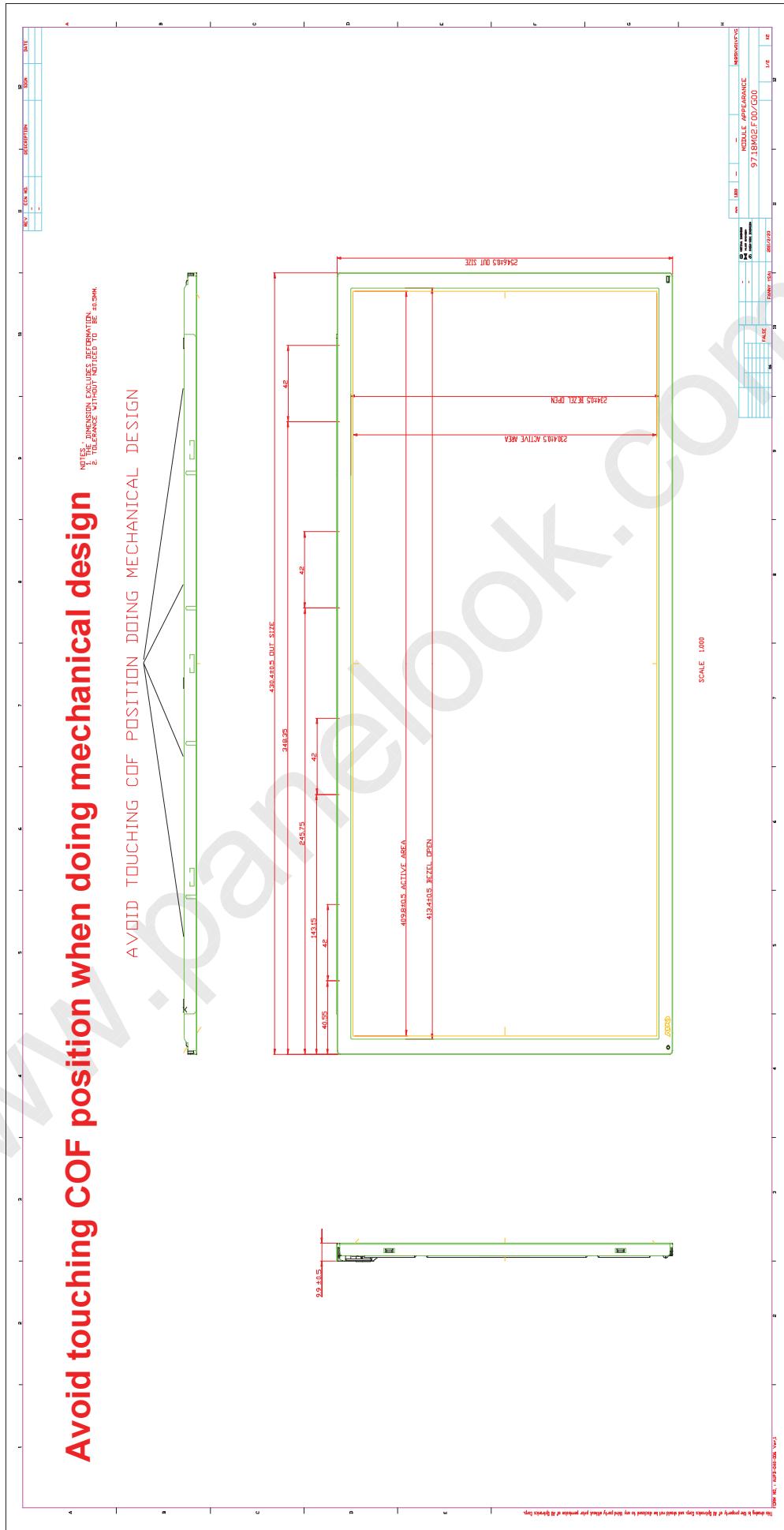
AVOID TOUCHING COF POSITION DOING MECHANICAL DESIGN

NOTES:
1. THE DIMENSION EXCLUDES DEFORMATION
2. TOLERANCE WITHOUT NOTICED TO BE ±0.5MM.

MODULE APPEARANCE
9718M02.F00/G00

SCALE 1.000

SIZE

294.6±0.5 OUT SIZE

234±0.5 BEZEL OPEN

230.4±0.5 ACTIVE AREA

430.4±0.5 OUT SIZE

348.35

245.75

143.15

40.55

42

42

42

42

409.8±0.5 ACTIVE AREA

413.4±0.5 BEZEL OPEN

9.9 ±0.5

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Product Specification

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BLU LABEL

SHIPPING LABEL

MODULE APPEARANCE

<4X> M3 USER HOLE
DP=3.7 MAX SCREW PENTRATION

document version 1.1

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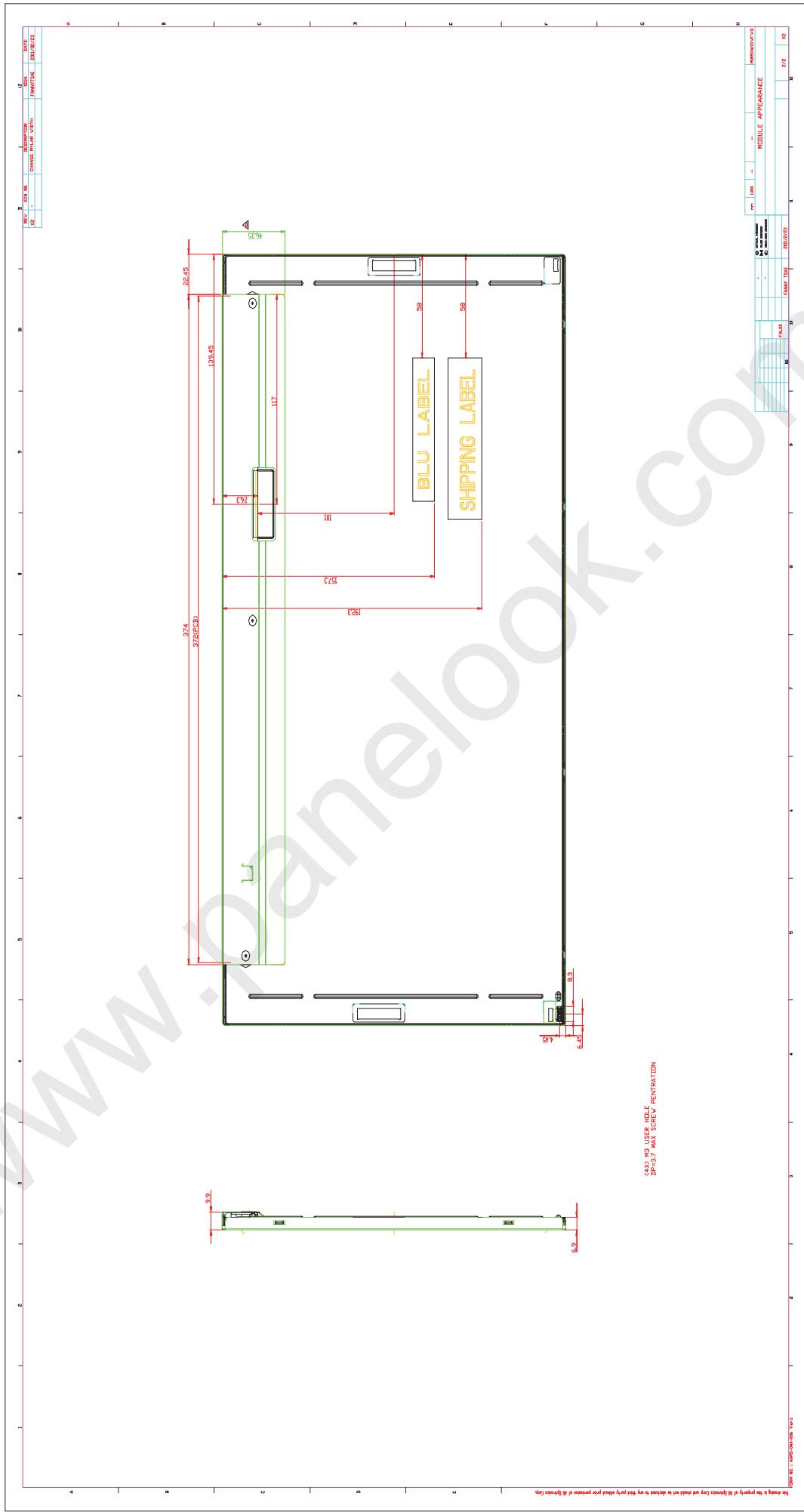
11. Packing Specification
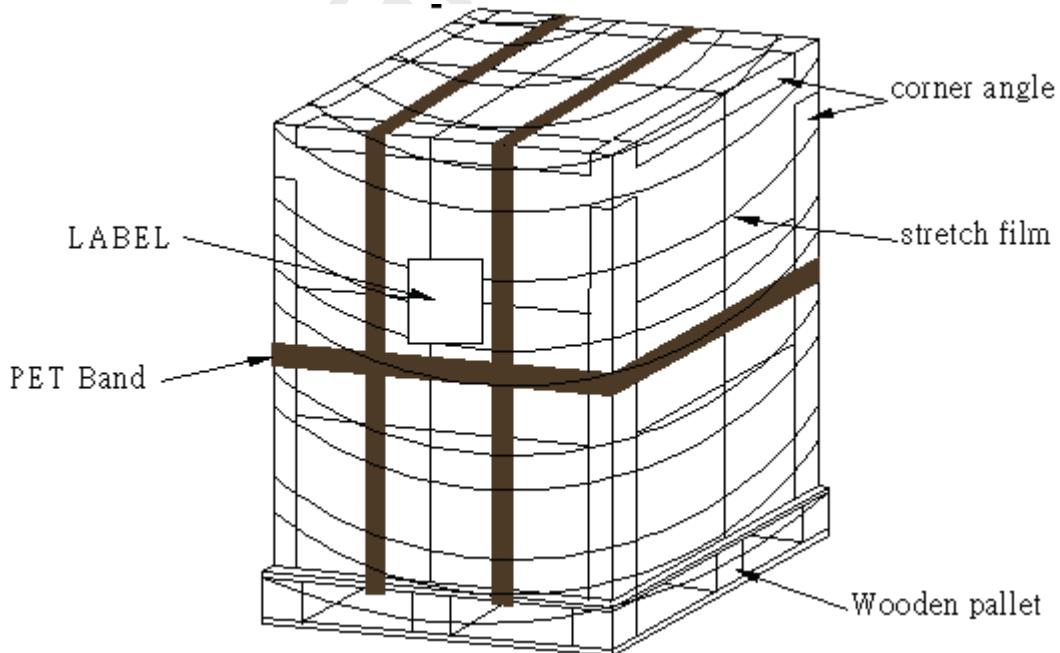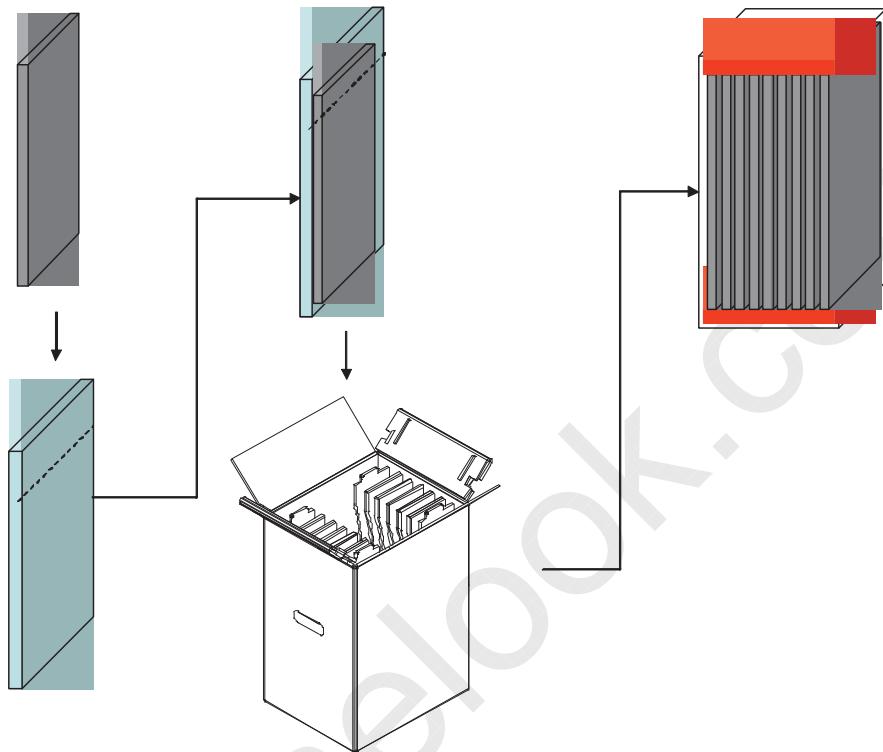
單層 **pallet** 打棧示意圖

Product Specification

M185XW01 VF

AU OPTRONICS CORPORATION

2. Pallet and shipment information

	Item	Q'ty	Dimension	Weight(kg)	Remark
1	Panel	1	430.37(H)mm x 254.6(V)mm x 9.9(D)mm	1.478	
2	Cushion	1	-	1.003	
3	Box	1	345(L)mm x 280(W)mm x 500(H)mm	1.18	without Panel & cushion
4	Packing Box	11 pcs/Box	345(L)mm x 280(W)mm x 500(H)mm	17	with panel & cushion
5	Pallet	1	1140(L)mm x 830(W)mm x 138(H)mm	12	
6	Pallet after Packing	26 boxes/pallet	1140(L)mm x 830(W)mm x 138(H)mm	420	

Note: The Specification column spans Q'ty, Dimension, Weight(kg).